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| INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> | | Application Number | 10/812,661 |
| | | Filing Date | 3/29/04 |
| | | First Named Inventor | Patricia J. Hoover |
| | | Art Unit | 2827 |
| | | Examiner Name | PHAN |
| Sheet | 1 | of | 2 |
| | | Attorney Docket Number | P18396 |

U. S. PATENT DOCUMENTS

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| Examiner Signature | Trong Phan | Date Considered | 11/30/05 |
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| NON PATENT LITERATURE DOCUMENTS | | | | | | |
|---------------------------------|-----------------------|---|--|--|--|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | | | | T ² |
| TP | 2 | PAOLO CAPPELLETTI, ET AL., "Flash Memories", 1999 - 3rd Printing 2001, Pgs. 250-253. | | | | |
| TP | 3 | INTEL DATASHEET, "1.8 Volt Intel StrataFlash® Wireless Memory with A/D-Multiplexed I/O, July 2003, Pgs. 1-100. | | | | |
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